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ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

<u>JG-3</u> APPLICATION NO. 09/731569		CONT/PRIOR D F	CLASS <del>338</del> 216	SUBCLASS <del>25</del> 100	ART UNIT <del>2032</del> 1765	PATENT DATE  EXAMINER Ahmed <del>S. Smith</del>
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## APPLICANTS

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Electrical resistor with at least two connection contact pads on a substrate with a least one recess, and process for its manufacture

PTO-2040  
12/99

## ISSUING CLASSIFICATION

[illegible]

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>	
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